## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicants:

Shubhendu S.

MUKHERJEE et al.

Confirmation NO.: 8856

SEP 0 1 2004

Serial No.:

09/837,995

Group Art Unit:

2114

Filed:

04/19/2001

Examiner:

A. L. Damiano

For:

Simultaneous And

Redundantly Threaded

Processor Store

Instruction Comparator

Docket No.:

200301818-1

Date: May 7, 2004

## **RESPONSE TO OFFICE ACTION DATED FEBRUARY 27, 2004**

Mail Stop Amendment Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

## **AMENDMENT**

Sir:

In response to the Office action of February 27, 2004, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 4 of this paper.

Amendments to the Drawings begin on page 10 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 11 of this paper.

An **Appendix** including amended drawing figures is attached following page 14 of this paper.

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